IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re U.S. Patent Application of)	7-3/ 10)
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TAKAGAWA et al.)	
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Application Number: To Be Assigned)	
Filed Companyonally Honorwith)	
Filed Concurrently Herewith)	
For: SEMICONDUCTOR DEVICE)	

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

This Information Disclosure Statement is submitted within three months of the filing date of the above-captioned U.S. patent application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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January 16, 2002

Form PTO 1449	ATTY. DOCKET NUMBER TSUT.0010	Serial Number To be assigned	PTO
U.S. Department of Commerce Patent and Trademark Office	Applicant Takagawa et al.		.s. 1626
Information Disclosure Statement by Applicant	FILING DATE Concurrently Herewith	GROUP	0 / 0 T
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U.S. Patent Documents

Examiner	DOCUMENT	DATE	Name	CLASS	SUBC	FILING DATE
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Foreign Patent Documents

Exami	DOCUMENT NUMBER	FILING DATE	Country	Class	Sub-	Translation	
ner Initial					CLASS	YES	No
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Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

Nick X. Sun, Alex Q. Huang and F.C. Lee, " $0.35\mu m$, $43~\mu\Omega cm^2$ power, $6~m\Omega$ power MOSFET to power future microprocessor," 1999 IEEE, pp. 77-80
Wharton McDaniel, "MOSFET selection is key to successful DC-DC conversion," Supplement to Electronic Design, December 6, 1999, pp. 158-165
Edward Stanford, "Device requirements for future CPU voltage regulators," Intel. Corp., pp. 1-6

Examiner		DATE CONSIDERED		

EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

PTO1449